Second Mixer IF for digital cellular phones BH4128FV

The BH4128FV is an IC equipped with internal second mixer and IF amplifier circuits, developed for use with digital cellular phones.

ApplicationsDigital cellular phones

Features

- 1) Equipped with internal mixer, IF, and RSSI circuits.
- 2) Can be operated at mixer input frequencies ranging from 10MHz to 200MHz.
- 3) IF amplifier gain is 100 dB.
- 4) Equipped with a battery power saving function.

●Absolute maximum ratings (Ta = 25 °C, for measurement circuit)

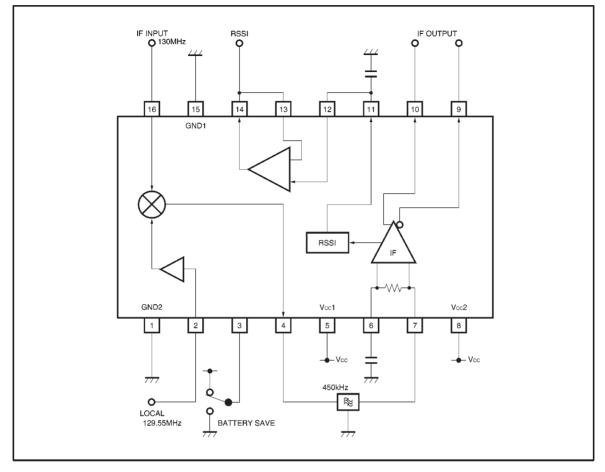
Parameter	Symbol	Limits	Unit
Power supply voltage	Vcc	7.0	V
Power dissipation	Pd	350*	mW
Storage temperature	Tstg	-55~+125	Ĵ

* Reduced by 3.5mW for each increase in Ta of 1°C over 25°C.

Recommended operating conditions (Ta = 25 °C, for measurement circuit)

Parameter	Symbol	Limits	Unit
Power supply voltage	Vcc	2.3~5.5	v
Operating temperature	Tpor	-40~+85	ΰ

Block diagram



BH4128FV

Pin descriptions

Pin No.	Function	Internal peripheral circuit	DC voltage (V)
1	GND pin 2	GND for MIX stage	GND
2	Local oscillation input pin Input from external oscillator	2 15p 15p 2.4k BIAS	_
3	Battery save pin "Pin 3 voltage" ≦0.2V : Battery save 2V≦ "Pin 3 voltage" ≦Vcc : Active	3 50k 777 777 777	_
4	Mixer output pin Connect ceramic filter Output impedance: 1 kΩ		Vcc-1.6
5	Vcc pin 1	Vcc for MIX stage and IF amplifier early stage	Vcc



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6 IF amplifier bypass pin Connect capacitor Voc=0.6 7 IF amplifier input pin Connect ceranic filter Output impedance: 1 KΩ Voc=0.6 8 Voc pin 2 Voc for IF later stage Voc 9, 10 IF amplifier output pin Postpat Voc for IF later stage Voc 11 Particle opposite-phase Voc=1.2 Voc=1.2 11 RSSI output pin Connect capacitor Voc=0.5 Voc=0.5	Pin No.	Function	Internal peripheral circuit	DC voltage (V)
7 Connect ceramic filter Couput Impedance: 1 kΩ \overrightarrow{r}	6			Vcc-0.6
9, 10 IF amplifier output pin Pins 9 and 10 are opposite-phase output Image: Construction of the phase output Image: Construction output I	7	Connect ceramic filter		Vcc-0.6
9,10 IF amplifier output pin Pins 9 and 10 are opposite-phase 9 10 9 11 RSSI output pin Connect capacitor 0.15	8	Vcc pin 2	Vcc for IF later stage	Vcc
11 RSSI output pin Connect capacitor 0.15	9, 10	Pins 9 and 10 are opposite-phase		Vcc-1.2
	11		11 50k ≶	0.15



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Pin No.	Function	Internal peripheral circuit	DC voltage (V)
12	Buffer amplifier non-inverting input pin		
13	Buffer amplifier inverting input pin		
14	Buffer amplifier output pin		_
15	GND pin 1	GND for IF stage and RSSI circuit	GND
16	Mixer input pin Connect first IF signal from DC cutoff		1.2



•Electrical characteristics (unless otherwise noted, $Ta = 25^{\circ}C$, Vcc = 3.0V

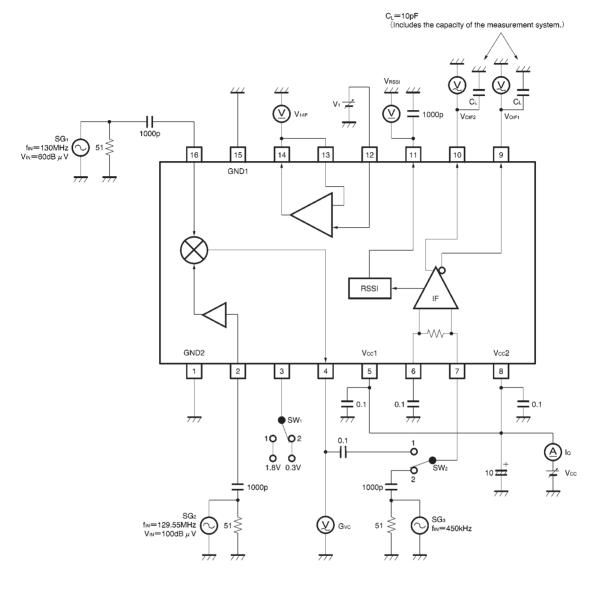
Signal source : fin (MIX) = 130MHz, fin (LO) = 129.55MHz, 100dB μV AC level to be indicated by termination)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Quiescent current	la	—	3.0	3.9	mA	VIN (LO)=100dB µ V
Battery save quiescent current	IQ (BS)	—	0	5	μA	
Battery save function input voltage	Vтн–н	2	-	Vcc	V	Active
battery save function input voltage	Vth-l	GND	-	0.2	v	Battery save
$\langle MIX$ - oscillator section $ angle$						
Mixer operating frequency	fміx	10	130	200	MHz	
Mixer conversion gain	Gvc	8.5	12.5	16.5	dB	VIN (MIX)=60dB μ V, RL=1k Ω
-1 dB compression output level	Vом	—	101		dB µ V	
3rd order intercept point	IРз	_	115	_	dB µ V	f1=130.05MHz, f2=130.10MHz
Noise figure	NF	_	8.5	_	dB	LC matching input
Mixer input admittance	YIN (MIX)	_	0.38+j2.75	-	ms	f=130MHz
Mixer output resistance	Ro (MIX)	_	1	-	kΩ	
Local input level	VIN (LO)	95	100	105	dB µ V	
Local input admittance	YIN (LO)	_	0.25+j3.65	_	ms	f=130MHz
(IF section)	1	1				
IF operating frequency	fı⊧	350	_	500	kHz	
IF amplifier gain	Gv	_	100		dB	
IF input resistance	RIN (IF)	_	1	_	kΩ	
IF output level	Voif	0.7	1	1.3	Vp-p	VIN (IF)=80dB µ V
IF output duty ratio	DR	45	50	55	%	VIN (IF)=80dB µ V, CL=10pF
⟨RSSI section⟩						
Output voltage 1	VRSSI1	_	0.15	0.4	V	No input
Output voltage 2	VRSSI2	1.0	1.2	1.4	V	V _{IN (IF)} =65dB μ V
Output voltage 3	VRSSI3	1.9	2.0	2.2	v	V _{IN (IF)} =100dB μ V
Dynamic range	DR	_	85	_	dB	
Output resistance	Ro (RSSI)	40	50	60	kΩ	
Rise time at power on	Том	_	140	-	μs	CL=1000pF, VIN (MIX)=60dB µ V
Fall time at power off	Toff	_	130	-	μs	CL=1000рF, VIN (MIX)=60dB µ V
RSSI rise time	TR	_	270	-	μs	CL=1000рF, VIN (MIX)=60dB µ V
RSSI fall time	TF	_	220	_	μs	CL=1000pF, VIN (MIX)=60dB μ V

ONot designed for radiation resistance.

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Measurement circuit



Units : Resistance : Ω Capacitance : μ F



Application example

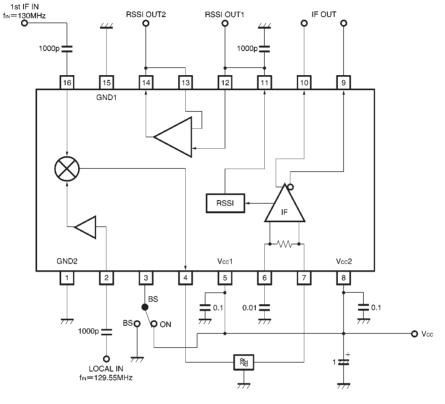


Fig. 2

Units : Resistance : Ω Capacitance : μ F



